REMARKS

Claim Rejections - 35 USC §102

Claims 1-25 are rejected unday 35 U.S.C. §102(e) as being anticipated by Gehman et al., (U.S. Patent No. 6,304,553, here thatter "Gehman").

As to claim 1, the Examiner states:

"As to claim 1, Gehman teaches a network interface for processing incoming messages sent by a gient device to a server,"

Applicant respectfully disagress. Gehman is for processing inside a personal computer and this is shown in Gehrald FIGs. 2 and 3 which show the client 200 (a personal computer) and the serial bus adapter \$19/300 (inside the personal computer), which contains the transmit and receive FIFOs 310 and 312. This is explained in Gehman col. 2, line 56, through col. 3, line 54:

"With reference now FIG. 2, a block diagram of a data processing system in which the present invention may be implemented is illustrated. Data processing system 200 is an example of a client computer. ...

An operating system plus on processor 202 and is used to coordinate and provide control of various components within data processing system 200

in FIG. 2. The operating system may be a commercially available operating system such as NT Windows of OS/2. ...

Turning now to FIG. 3. Serial bus adapter 300...includes a number of components used to provide in interface between PCI local bus 302 and a 1394 serial bus 304. Serial bus adapter in the depicted example may be serial bus adapter 219 from FIG. 2. Serial bus adapter 300 includes a physical layer device 306, a link 308, a transmit FIFO unit 310, a receive FIFO unit 312, a DMA engine 314, and a hos interface 316." [underlining and deletions for clarity] clarity]

Applicant respectfully travelies the rejections since the Applicant's claimed combination, as exemplified in claim includes the limitation not disclosed in Gehman of:

- "A network interface for prodesting incoming messages sent by a client device to a server, comprising
 - a First-In-First-Out (FIFO) buffer adapted to receive the incoming messages and pressure assemble the incoming messages from a serial to a parallel form."

The Examiner states:

"As to claim 1, Gehrhan teaches a network interface for processing incoming messages sent by a client device to a server, comprising:

a First-In-First-Out (FIFO) buffer adapted to receive the incoming messages and to assemble the incoming messages from a serial to a parallel form (col. 1, lines 39-47, col. 4 lines 11-25);" [underlining for clarity]

Applicant respectfully disagress. As explained in Gehman col. 2, line 56, through col. 3, line 54, cited above, Gehman teaches how signals are processed within a client device not how signals from a client device to a server are processed. It is respectfully submitted that the nature of the messages in Gehman de different; e.g., PCI local bus to 1394 signals versus serial to parallel Hypertext Transfer Protocol messages.

The Examiner continues:

"[As to claim 1, Gentlem teaches a network interface for processing incoming messages sent by a client device to a server, comprising:]

a regular-expression pattern matching circuit connected to the FIFO buffer, the regular-expression pattern matching circuit adapted to, concurrent with the assembly of the incoming messages from a serial to a parallel form, recognize Hypertext Transfer Protocol (HTTP) message headers embedded in the incoming messages, parse recognized HTTP message headers into parsed HTTP message headers, and provide the parsed HTTP message headers to the server (col. 1, lines 13-24, col. 1, lines 39-47)." [insertion and deletion for clarity]

Applicant respectfully disagrass. Gehman does not describe a "regular-expression pattern matching circuit" or recognizing "Hypertext Transfer Protocol (HTTP) message headers" in Gehman col. 1, lines 13-14, which states:

"...The packet includes a header and a payload. The header includes information identifying the target, payload type, source, and various control data as specified by the projectol while the payload holds the data that is transmitted. When a packet is received at a data processing system, the packet is parsed to see if the packet is intended for the data processing system."

Gehman also does not describe a "regular-expression pattern matching circuit" or recognizing "Hypertext Transfer Propolo (HTTP) message headers" in Gehman col. 1, lines 39-47, which states:

"Currently, on a data processing system using the 1394 standard, a link, providing the interface to the 1394 serial bus, must parse a received packet to determine whether to accept the packet and whether to acknowledge acceptance of a packet. If the packet is accepted, the link places the packet into

a buffer configured as a first-in first-out (FIFO) buffer. On the other side of the FIFO buffer in the data processing system is a DMA engine that removes the packet and parses the packet in a manner similar to the link."

Gehman also teaches away from the above by indicating the problems with the above and that an improved system is desirable by referring to the above and stating in the last paragraph of Gehman at col. 1, lines 48-51:

"This mechanism results in redundant functions and circuitry. Therefore, it would be adjustageous to have an improved method and apparatus for receiving packets on a data processing system."

Further, with regard to claim! it is respectfully submitted that Gehman does not disclose a combination that includes the claimed limitation of:

"...the regular-expression partern matching circuit adapted to, concurrent with the assembly of the incoming messages from a serial to a parallel form, recognize Hypertext Transfer Protocol (HTTP) message headers embedded in the incoming messages, parse recognized HTTP message headers into parsed HTTP message headers, and provide the parsed HTTP message headers to the server."

As seen in Gehman col. 1, light 13-24, col. 1, lines 39-47, cited by the Examiner and quoted above, there is no mention in the concurrent operations claimed. This has the advantages described in Specification page 7, line 30, through page 8, line 4:

"The present invention allows regular-expression pattern matching to be performed concurrently with the serial-to-parallel message-component assembly, such that message protocol header analysis can be done while incoming data is being clocked into the FIFO buffer 104. In this way, the message protocol header analysis incurs no extra time delay, and a compact representation of the extracted message protocol header information is ready for transfer to the web server 26 at the same time as the ordinary, assembled parts of the message. This technique may be referred to as "latency hiding"; i.e., overlapping some parts of a series of information processing steps in order to reduce the sequence's overall delay."

Based on the above, it is respectfully submitted that claim 1 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because:

"Anticipation requires the presence in a single prior art reference disclosure of each and every element of the claimed invention, arranged as in the claim." [emphasis added] Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick Co. (730 F.2d 1452, 221 USPQ 481, 485 (Fed. Cir. 1984)(citing Connell v. Sears Roebuck & Co., 722 F.2d 1542, 220 USPQ 193 (Fed Dir. 1983)))

As to claim 2, Applicant respectfully traverses the rejections since the Applicant's claimed combination includes the limitation not disclosed in Gehman of:

"a logic circuit connected to the FIFO buffer, the logic circuit adapted to provide a response message to the client device based on a content of the recognized HTTP message headers."

The Examiner states:

"As to claim 2, Gehing teaches the network interface as claimed in claim 1 further including: a logic circuit connected to the FIFO buffer, the logic circuit adapted to provide a response message to the client device based on a content of the recognized HTTP message headers (col. 1, lines 55-63, col. 2, lines 43-55, col. 3, lines 12-15)."

Applicant respectfully disagress because no logic circuit is mentioned in the Examiner cited sections of Gehman. Further claimfication is requested pursuant to 37 CFR §1.104(c)(2) since no logic circuit having the claimed functions is described in Gehman col. 1, lines 55-63, which states:

"The present inventical provides a method and apparatus for receiving packets from a bus. A packet is received at an interface to the bus. The packet is parsed, and a determination is made whether to retain the packet from the parsing of the packet. The packet is placed in a buffer with a header. The packet is moved from the taffer to another bus using information located within the header, wherein reseated parsing of the packet to move the packet to another bus is unnecessary."

Similarly, Gehman col. 2, line 43-55, states:

"In the depicted example, a server 104 is connected to network 102 along with storage unit 106 In addition, clients 108, 110, and 112 also are connected to a network 102. These clients 108, 110, and 112 maybe, for example, personal computers or network computers. For purposes of this application, a network computer is any computer, coupled to a network, which receives a program or other application from another computer coupled to the network. In the depicted example, server 104 provides data, such as boot files, operating system images, and applications to clients 108-112. Clients 108, 110, and 112 are clients to server 104. Distributed data processing system 100 may include additional servers tilients, and other devices not shown.

Similarly, Gehman col. 3, line 2-15, states:

"The LAN may be implemented as a serial bus architecture in the depicted example. In such a case, the processes of the present invention may be implemented in LAN adapter 210."

Based on the above, it is respectfully submitted that claim 2 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick Co., supra.

As to claim 3, Applicant respectfully traverses the rejections since the Applicant's claimed combination includes the limitation not disclosed in Gehman of:

"the regular-expression pattern matching circuit is further adapted to provide to the server the parses TTP message headers in a compact form."

The Examiner states:

"As to claim 3, Gehman teaches the network interface as claimed in claim 1 wherein: the regular expression pattern matching circuit is further adapted to provide to the saver the parsed HTTP message headers in a compact form (col. 1, lines 13 14, col. 1, lines 39-47)."

Applicant respectfully disagrees because no regular-expression matching circuit is mentioned in the Examiner cited sections of Gehman. Further clarification is requested pursuant to 37 CFR §1.104(c)(2). The relevant sections of Gehman are not quoted since the lack of the claimed element will be evident upon inspection as is the case for claim 2.

Based on the above, it is respectfully submitted that claim 3 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick Co., supra.

As to claim 4, Applicant respectfully traverses the rejections since the Applicant's claimed combination includes the limitation not disclosed in Gehman of:

"the regular-expression pattern matching circuit is further adapted to provide to the server incoming messages that cannot be recognized by the regular-expression pattern matching circuit."

The Examiner states:

"As to claim 4, Gehran teaches the network interface as claimed in claim 1 wherein: the regular expression pattern matching circuit is further adapted to provide to the server incoming messages that cannot be recognized by the regular-expression pattern matching circuit (col. 1, lines 13-24, col. 1, lines 39-47)."

Applicant respectfully disagrees because no regular-expression matching circuit is mentioned in the Examiner cited sections of Gehman. Further clarification is requested pursuant to 37 CFR §1.104(c)(2). The relevant sections of Gehman are not quoted since the lack of the claimed element will be evident upon inspection as is the case for claim 2.

Based on the above, it is respectfully submitted that claim 4 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick 30., supra.

As to claim 5, Applicant respectfully traverses the rejections since the Applicant's claimed combination includes the limitation not disclosed in Gehman of:

"the regular-expression pattern matching circuit is implemented by a technique consisting of hardward spftware, and a combination thereof."

The Examiner states:

"As to claim 5, Gehard teaches the network interface as claimed in claim 1 wherein: the regular-expression pattern matching circuit is implemented by a technique consisting of hardware, software, and a combination thereof (col. 1, lines 39-48)."

Applicant respectfully disagrees because no regular-expression matching circuit is mentioned in the Examiner cited sections of Gehman. Further clarification is requested pursuant to 37 CFR §1.104(c)(2). The relevant section of Gehman is not quoted since the lack of the claimed element will be examiner upon inspection as is the case for claim 2.

Based on the above, it is respectfully submitted that claim 5 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick Co., supra.

As to claim 6, Applicant respectfully traverses the rejections since the Applicant's claimed combination includes the limitation not disclosed in Gehman of:

"the HTTP message headers include HTTP cookies."

The Examiner states:

> "As to claim 6, Gehring teaches the network interface as claimed in claim 1 wherein: the HTTP pressage headers include HTTP cookies (col. 1, lines 13-24)."

Applicant respectfully disagnish because no HTTP message headers or HTTP cookies are mentioned in the Examiner cited sections of Gehman. Further clarification is requested pursuant to 37 CFR §1.104(c)(2). The relevant section of Gehman is not quoted since the lack of the claimed element will be differt upon inspection as is the case for claim 2.

Based on the above, it is respectfully submitted that claim 6 is allowable under 35 U.S.C. 102(e) as not being anticipaled by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick Ch., supra.

As to claim 7, Applicant respectfully traverses the rejections since the Applicant's claimed combination includes the limitations not disclosed as in claim 1 and the additional limitations not disclosed in Gehman

"the HTTP message headers include HTTP cookies, and the regular-extression pattern matching circuit is implemented by a technique consisting of hardware, software, and a combination

thereof and
a logic circuit connected to the FIFO buffer, the logic circuit adapted to
provide a response message to the client device based on a content of
the recognized HTTP massage header."

The Examiner states:

- ""As to claim 7, German teaches a network interface for processing incoming messages sent by a stant device to a server, comprising:

 a First-In-First-Out HFO) buffer adapted to receive the incoming messages and to assemble the incoming messages from a serial to a parallel form (col. 1, lines 39-47, col. ines 11-25);

 a regular-expression pattern matching circuit connected to the FIFO buffer, the regular-expression pattern matching circuit adapted to, concurrent with the assembly of the incoming messages from a serial to a parallel form, recognize Hypertext Transfer Protocol (HTTP) message headers embedded in the incoming messages, parser recognized HTTP message headers into parsed HTTP message headers, provide the parsed HTTP message headers in a compact form to the server, and provide to the server incoming messages that cannot be recognized by the regular-expression pattern matching circuit (col. 1, lines 13-24, col. 1, lines 39-47), wherein:

 the HTTP message headers include HTTP cookies (col. 1, lines 13-24), and

and

> the regular-expression pattern matching circuit is implemented by a technique consisting of hardwise, software, and a combination thereof (col.1, lines 39-48); and

> a logic circuit connected to the FIFO buffer, the logic circuit adapted to provide a response message to the client device based on a content of the recognized HTTP message hapter (col. 1, lines 55-63, col. 2, lines 43-55, col. 3, lines 12-15). 11.""

Applicant respectfully disagraph for the same reasons as in claim 1 and because no HTTP cookies, regular-expression matching circuit, or claimed logic circuit is mentioned in the Examiner cited sections of Gehn Further clarification is requested pursuant to 37 CFR §1.104(c)(2). The relevant sections Gehman are not quoted since the lack of the claimed element will be evident upon inspection as is the case for claim 2.

Based on the above, it is respectfully submitted that claim 7 is allowable under 35 U.S.C. 102(e) as not being anticipality by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick 11., supra.

As to claim 8, Applicant resident fully traverses the rejections since the Applicant's claimed combination includes the limitations not disclosed in Gehman of:

"a central processing unit (Chira

- stored therein; and
- a network interface for processing incoming messages sent by the client device to the server, the network interface including:

 a First-In-First-Out (FFO) buffer adapted to receive the incoming messages and to assemble the incoming messages from a serial to a parallel form,
- a regular-expression pattern in arching circuit connected to the FIFO buffer, the regular-expression pattern matching circuit adapted to, concurrent with the assembly of the incoming messages from a serial to a parallel form, recognize Hypertext II ansfer Protocol (HTTP) message headers embedded in the incoming messages, parse recognized HTTP message headers into parsed FITP message headers, and provide the parsed HTTP message headers to the CPU and the memory, wherein the HTTP message headers include HTTP cookies."

The Examiner states:

"As to claim 8, Gehind teaches a server for providing services to a client device, comprising:

a central processing unit (CPU) (Fig. 1, col. 2, lines 43-55);

- a bus connected to the CPU (Fig. 1, col. 2, lines 43-55);
 a memory connected to the bus, the memory having a server application program stored therein (Fig. 1, col. 2, lines 43-55); and
 a network interface for processing incoming messages sent by the client device to the server, the network interface including:
 a First-In-First-Out (FG) buffer adapted to receive the incoming messages and to assemble the incoming messages from a serial to a parallel form (col. 1, lines 39-47, col. Lines 11-25), and
 a regular-expression pattern matching circuit connected to the FIFO buffer, the regular-expression pattern matching circuit adapted to, concurrent with the assembly of the incoming messages from a serial to a parallel form, recognize Hypertext Transfer potocol (HTTP) message headers embedded in the incoming messages, pars recognized HTTP message headers into parsed HTTP message headers, and provide the parsed HTTP message headers to the CPU and the memory, where it the HTTP message headers include HTTP cookies (col. 1, lines 13-24, col. 1, lines 39-47, (col. 1, lines 13-24))."

Applicant respectfully disagrass: As explained, Gehman discloses a section of a client device rather than a server as claimed so the individual components are not connected as claimed and elements are missing as explained above. Further clarification is requested pursuant to 37 CFR §1.104(c)(2). The relevant sections of Gehman are not quoted since the lack of the claimed element or the element connected as described will be evident upon inspection as is the case for claim 2.

Based on the above, it is respectfully submitted that claim 8 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick J., supra.

As to claim 9, Applicant respectfully traverses the rejections since the Applicant's claimed combination includes the limitation not disclosed in Gehman of:

"a logic circuit connected to the FIFO buffer, the logic circuit adapted to provide a response making to the client device based on a content of the recognized HTTP message headers."

The Examiner states:

"As to claim 9, Geliain teaches the server as claimed in claim 8 further including: a logic circuit connected to the FIFO buffer, the logic circuit adapted to provide a response message to the client device based on a content of the recognized HTTP message headers (col. 1, lines 55-63, col. 2, lines 43-55, col. 3, lines 12-15)."

Applicant respectfully disagned because no logic circuit is mentioned in the Examiner cited sections of Gehman. Further characterisation is requested pursuant to 37 CFR §1.104(c)(2). The relevant section of Gehman is requested since the lack of the claimed element will be evident upon inspection as is the case for claim 2.

Based on the above, it is respectfully submitted that claim 9 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick 113., supra.

As to claim 10, Applicant respectfully traverses the rejections since the Applicant's claimed combination includes the limitation not disclosed in Gehman of:

"the regular-expression pattern matching circuit is further adapted to provide to the CPU and the memory the parsed HTTP message headers in a compact form."

The Examiner states:

"As to claim 10, Gentian teaches the server as claimed in claim 8 wherein: the regular-expression pattern matching circuit is further adapted to provide to the CPU and the summer the parsed HTTP message headers in a compact form (col. 1, lines 15, 24, col. 1, lines 39-47)."

Applicant respectfully disables because no regular-expression pattern matching circuit is mentioned in the Examination cited sections of Gehman. Further clarification is requested pursuant to 37 CFR §1.1 (c)(2). The relevant section of Gehman is not quoted since the lack of the claimed element will be evident upon inspection as is the case for claim 2.

Based on the above, it is respectfully submitted that claim 10 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick CD., supra.

As to claim 11, Applicant respectfully traverses the rejections since the Applicant's claimed combination includes the limitation not disclosed in Gehman of:

"the regular-expression pattern matching circuit is further adapted to provide to the CPU and the innemory incoming messages that cannot be recognized by the regular-expression pattern matching circuit."

The Examiner states:

"As to claim 11, Goddinan teaches the server as claimed in claim 8 wherein:

the regular-expression rattern matching circuit is further adapted to provide to the CPU and the memory incoming messages that cannot be recognized by the regular-expression pattern matching circuit (col. 1, lines 13-24, col. 1, lines 39-47)."

Applicant respectfully disagress because no regular-expression pattern matching circuit is mentioned in the Examinat cited sections of Gehman. Further clarification is requested pursuant to 37 CFR §1.14 (c)(2). The relevant section of Gehman is not quoted since the lack of the claimed element will be evident upon inspection as is the case for claim 2.

Based on the above, it is respectfully submitted that claim 11 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick S., supra.

As to claim 12, Applicant respectfully traverses the rejections since the Applicant's claimed combination includes the limitation not disclosed in Gehman of:

"the HTTP message headers include HTTP cookies."

The Examiner states:

"As to claim 12, Gentlan teaches the server as claimed in claim 8 wherein:

the HTTP message insiders include HTTP cookies (col. 1, lines 13-24)."

Applicant respectfully disagres because no HTTP message headers or HTTP cookies are mentioned in the Examiner cited sections of Gehman. Further clarification is requested pursuant to 37 CFR §1.104(c)(2). The relevant section of Gehman is not quoted since the lack of the claimed element will be expent upon inspection as is the case for claim 2.

Based on the above, it is respectfully submitted that claim 12 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick C., supra.

As to claim 13, Applicant respectfully traverses the rejections since the Applicants' claimed combination includes the limitations not disclosed as in claim 1 and the additional limitations not disclosed in Gehman

- a central processing unit (CPIII)
- a bus connected to the CPU;
- a memory connected to the six, the memory having a server application program stored therein; and
- stored therein; and
 a network interface for processing incoming messages sent by the client device to the server, the network in face including:
 a First-In-First-Out (IFO) buffer adapted to receive the incoming messages and to assemble the incoming messages from a serial to a parallel form, a regular-expression pattern matching circuit connected to the FIFO buffer, the regular-expression matching circuit adapted to, concurrent with the assembly of the incoming messages from a serial to a parallel form, recognize Hamiltonian messages from a serial to a parallel form, recognize Hamiltonian messages from a serial to a parallel form, recognize Hamiltonian messages from a serial to a parallel form, recognize Hamiltonian messages from a serial to a parallel form, recognize Hamiltonian messages from a serial to a parallel form, recognize Hamiltonian messages from a serial to a parallel form, recognized HTTP message headers, provide the parsed HTTP message headers incoming messages, parse recognized HTTP message headers, provide the parsed HTTP message headers in a compact form to the CPU and the memory, and provide to the TU and the memory incoming messages that cannot be recognized by the regular-expression pattern matching circuit, wherein: the HTTP message headers include HTTP cookies, and the regular-expression pattern matching circuit is implemented by a technial consisting of hardware, software, and a combination thereo and a logic circuit connected to the FIFO buffer, the logic circuit adapted to provide a response message to the client device based on a content of the recognized HTTP message headers."

 The Examiner states:

The Examiner states:

"As to claim 13, Gelinan teaches a server for providing services to a client device, comprising:

- client device, comprising:

 a central processing up (CPU) (Fig. 1, col. 2, lines 43-55);

 a bus connected to the PU (Fig. 1, col. 2, lines 43-55);

 a memory connected to the bus, the memory having a server application program stored the client (Fig. 1, col. 2, lines 43-55); and

 a network interface for processing incoming messages sent by the client device to the server, the network interface including:

 a First-In-First-Out (FO) buffer adapted to receive the incoming messages and to assemble to incoming messages from a serial to a parallel form (col. 1, lines 39-47, col. lines 11-25),

 a regular-expression aftern matching circuit connected to the FIFO buffer, the regular-expression pattern matching circuit adapted to, concurrent with the assembly of the incoming messages from a serial to a parallel form, recognize Hypertext Transfer protocol (HTTP) message headers embedded in the incoming messages, pare incognized HTTP message headers into parsed HTTP message headers, pareited the parsed HTTP message headers in a

> compact form to the CPU and the memory, and provide to the CPU and the memory incoming messages that cannot be recognized by the regular-expression pattern matching circuit (col. 1, lines 13-24, col. 1, lines 39-47), wherein:

the HTTP message is aders include HTTP cookies, and the regular-expression pattern matching input is implemented by a technique consisting of hardware, software, and a sumbination thereof (col. 1, lines 39-48), and a logic circuit connected to the FIFO buffer, the logic circuit adapted to provide a response message is the client device based on a content of the recognized HTTP message harders (col. 1, lines 55-63, col. 2, lines 43-55, col. 3, lines 12-15). 17."

Applicant respectfully disagrams for the same reasons as in claim 1 and because no regular-expression matching circuit is claimed logic circuit is mentioned in the Examiner cited sections of Gehman. Further callification is requested pursuant to 37 CFR §1.104(c)(2). The relevant sections of Gehman ard to quoted since the lack of the claimed element will be evident upon inspection as is the case for claim 2.

Based on the above, it is resultfully submitted that claim 13 is allowable under 35 U.S.C. 102(e) as not being anticipally by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick 11., supra.

As to claim 14, Applicant respectfully traverses the rejections since the Applicants' claimed combination includes the limitations not disclosed in Gehman of:

"a client device; and

- a server connected to the climated device for providing services to the client device, the server including:
 - a central processing up (CPU), a bus connected to the PU,

 - the bus, the memory having a server application a memory connected
- a memory connected to the bus, the memory having a server application program store in erein, and a network interface for cocessing incoming messages sent by the client device to the server, and network interface including:

 a FIFO buffer adapted to receive the incoming messages and to assemble the incoming messages from a serial to a parallel form, and a regular-expression pattern in thing circuit connected to the FIFO buffer, the regular-expression pattern in matching circuit adapted to, concurrent with the assembly of the introducing messages from a serial to a parallel form, recognize Hypertext Transfer Protocol (HTTP) message headers embedded in the incoming messages, parse recognized HTTP message

headers into parsed HTTP message headers, and provide the parsed HTTP message header to the CPU and the memory."

The Examiner states:

"As to claim 14, hman teaches a communications network, comprising:

a client device (Fig. 1 21. 2, lines 43-55);
and a server connected to the client device for providing services to the client device (Fig. 1, col. 2, lines 43-55), the server including:
a central processing up (CPU) (Fig. 1, col. 2, lines 43-55),
a bus connected to the IPU (Fig. 1, col. 2, lines 43-55),
a memory connected to the bus, the memory having a server application program stored berein, and a network interface for processing incoming messages sent by the client device to the server (Fig. 1, col. 2, lines 43-55), the network interface including:

a FIFO buffer adapted to receive the incoming messages and to assemble the incoming messages from a serial to a parallel form (col. 1, lines 39-47, col. 4, lines 11-25), and it

a regular-expression pattern matching circuit connected to the FIFO buffer, the regular-expression pattern matching circuit adapted to, concurrent with the assembly of the including messages from a serial to a parallel form, recognize Hypertext Transfer stotocol (HTTP) message headers embedded in the incoming messages, paragrecognized HTTP message headers into parsed HTTP message headers, and provide the parsed HTTP message headers to the CPU and the memory (col. 1 lines 13-24, col. 1, lines 39-47)."

Applicant respectfully disagnet for the same reasons as in claim 1 and because the relationship between the server and client device is mot mentioned in the Examiner cited sections of Gehman. Further clarification is requested pursuant to 37 CFR \$1.104(c)(2). The relevant sections of Gehman are no uoted since the lack of the claimed element will be evident upon inspection as is the case or claim 2.

Based on the above, it is resultfully submitted that claim 14 is allowable under 35 U.S.C. 102(e) as not being anticipal by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick 12, supr.

As to claim 15, Applicant registicifully traverses the rejections since the Applicants' claimed combination includes the limitation not disclosed in Gehman of:

"a logic circuit connected to the FIFO buffer, the logic circuit adapted to provide a response making to the client device based on a content of the recognized HTTP resistage headers."

The Examiner states:

"As to claim 15, Graman teaches the communications network as claimed in claim 14 further is inding:

a logic circuit connect to the FIFO buffer, the logic circuit adapted to provide a response message to the client device based on a content of the recognized HTTP message have response to the client device based on a content of the recognized HTTP message have response to 12 15 " 3, lines 12-15)."

Applicant respectfully disagned because no logic circuit is mentioned in the Examiner cited sections of Gehman. Further defication is requested pursuant to 37 CFR §1.104(c)(2). The relevant section of Gehman is quoted since the lack of the claimed element will be evident upon inspection as is the case or claim 2.

Based on the above, it is resetfully submitted that claim 15 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick

As to claim 16, Applicant rejectfully traverses the rejections since the Applicants' claimed combination includes the li ation not disclosed in Gehman of:

"the regular-expression patternatching circuit is further adapted to provide to the CPU and the company the parsed HTTP message headers in a compact form."

The Examiner states:

"As to claim 16, Comman teaches the communications network as

claimed in claim 14 wherein it the regular-expression sattern matching circuit is further adapted to provide to the CPU and the themory the parsed HTTP message headers in a compact form (col. 1, lines 1 24, col. 1, lines 39-47)."

Applicant respectfully disa s because no regular-expression pattern matching circuit is mentioned in the Examinal cited sections of Gehman. Further clarification is requested pursuant to 37 CFR §1.1 (2). The relevant section of Gehman is not quoted since the lack of the claimed elemental ill be evident upon inspection as is the case for claim 2.

Based on the above, it is resetfully submitted that claim 16 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick 13., supra.

Based on the above, it is restrictfully submitted that claim 25 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick Co., supra.

As to claim 17, Applicant respectfully traverses the rejections since the Applicants' claimed combination includes the limitation not disclosed in Gehman of:

"the regular-expression pattern matching circuit is further adapted to provide to the CPU and the memory incoming messages that cannot be recognized by the regular-expression pattern matching circuit."

The Examiner states:

"As to claim 17, Gaman teaches the communications network as claimed in claim 14 wherein.

the regular-expression pattern matching circuit is further adapted to provide to the CPU and transmemory incoming messages that cannot be recognized by the regular-expression pattern matching circuit (col. 1, lines 13-24, col. 1, lines 39-47)."

Applicant respectfully disagrees because no regular-expression pattern matching circuit is mentioned in the Examines cited sections of Gehman. Further clarification is requested pursuant to 37 CFR §1.1 (c)(2). The relevant section of Gehman is not quoted since the lack of the claimed elements will be evident upon inspection as is the case for claim 2.

Based on the above, it is rejectfully submitted that claim 17 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick D., supra.

As to claim 18, Applicant rejectfully traverses the rejections since the Applicants' claimed combination includes the limitation not disclosed in Gehman of:

"the HTTP message headers ude HTTP cookies."

The Examiner states:

"As to claim 18, Graman teaches the communications network as claimed in claim 14 wherein the HTTP message the lers include HTTP cookies (col. 1, lines 13-24)."

Applicant respectfully disagned because no HTTP message headers or HTTP cookies are mentioned in the Examiner cite dections of Gehman. Further clarification is requested pursuant to 37 CFR §1.104(c)(2). The relevant section of Gehman is not quoted since the lack of the claimed element will be the ent upon inspection as is the case for claim 2.

Based on the above, it is residually submitted that claim 18 is allowable under 35 U.S.C. 102(e) as not being anticipal by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick D., supra.

As to claim 19, Applicant respectfully traverses the rejections since the Applicants' claimed combination includes the literations not disclosed as in claim 1 and the additional limitations not disclosed in Gehman

- a client device; and
- a server connected to the client device for providing services to the client device, the server including:

 - a central processing up (CPU),
 a bus connected to the PU,
 a memory connected to the PU,
 a memory connected to the program store to the server,
 a network interface of the server,
 a First-In-Fire the put (FIFO) buffer adapted to receive the incoming message and to assemble the incoming messages from a serial to a particle form,
 a regular-exp sion pattern matching circuit connected to the FIFO buffer to, control to a parallel form, recognize Hypertext Transfer Protocon HTTP) message headers embedded in the incoming message headers, provide the parsed HTTP message headers into parsed the parsed HTTP message headers and to the CPU and the memory, and headers in a compact form to the CPU and the memory, and provide to the CPU and the memory incoming messages that cannot be recognized by the regular-expression pattern matching circuit, wherein:

 the HT | message headers include HTTP cookies, and the recognized by the regular-expression pattern matching circuit is implemented.

 - the resilier-expression pattern matching circuit is implemented a technique consisting of hardware, software, and a content of the response message to the client device based on a content of the recognized HTTP message headers."

The Examiner states:

comprising:

a client device (Fig. 1 2). 2, lines 43-55); and
a server connected the client device for providing services to the client device, the server including:

col. 2, lines 43-55),

wherein:

and

lines 39-48), and

3, lines 12-15). 23."

HTTP cookies regular-expression n element will be evident upon inspec

GmbH v. American Hoist & Derrick Ja., supra.

"As to claim 19, the hman teaches a communications network

a central processing (CPU), a bus connected to the CPU (Fig. 1,

col. 2, lines 43-55),

a memory connected to the bus, the memory having a server application program stored the in (Fig. 1, col. 2, lines 43-55), and

a network interface for coessing incoming messages sent by the client device to the server, the network interface including:

a First-In-First-Out interface including:

a First-In-First-Out interface including:

a First-In-First-Out interface including:

a regular-expression incoming messages from a serial to a parallel form (col. 1, lines 39-47, col. interface including:

a regular-expression interface including:

a First-In-First-Out interface including:

a regular-expression interface including:

a parallel form a serial to a parallel form, otocol (HTTP) message headers embedded in ecognized HTTP message headers into parsed the parsed HTTP message headers in a compact form to the CPU at the memory, and provide to the CPU and the memory incoming message headers in a compact form to the CPU and the memory incoming message headers in a compact form to the CPU and the memory incoming message headers in a compact form to the CPU and the memory incoming message headers in a compact form to the CPU and the memory incoming message headers in a compact form to the CPU and the memory incoming message headers in a compact form to the CPU and the memory incoming message headers in a compact form to the CPU and the memory incoming message headers in a compact form to the CPU and the memory incoming message headers in a compact form to the CPU and the memory incoming message headers in a compact form to the cPU and the memory incoming message headers i

the HTTP message has rs include HTTP cookies (col. 1, lines 13-24),

the regular-expression attern matching circuit is implemented by a technique consisting of hard the, software, and a combination thereof (col.1,

a logic circuit connect to the FIFO buffer, the logic circuit adapted to provide a response message to the client device based on a content of the recognized HTTP message haders (col. 1, lines 55-63, col. 2, lines 43-55, col.

Applicant respectfully disages for the same reasons as in claim 1 and because no hing circuit or claimed logic circuit is mentioned in the Examiner cited sections of Gehn Further clarification is requested pursuant to 37 CFR §1.104(c)(2). The relevant sections Gehman are not quoted since the lack of the claimed as is the case for claim 2.

Based on the above, it is residentfully submitted that claim 19 is allowable under 35 U.S.C. 102(e) as not being anticipal by Gehman because of Lindemann Maschinenfabrik

As to claim 20, Applicant resectfully traverses the rejections since the Applicants' claimed combination includes the limitation not disclosed in Gehman of:

"receiving the incoming messages using a First-In-First-Out (FIFO) buffer; assembling the incoming messages from a serial to a parallel form using the FIFO buffer: and

The Examiner states:

to a parallel form:

39-47),

13-24, col. 1, lines 39-47)."

case for claim 2.

concurrent with the assemble of the incoming messages from a serial to a parallel

recognizing Hypertex transfer Protocol (HTTP) message headers embedded in the incomist messages received by the FIFO buffer using a regular-expression pattern matching circuit, parsing recognized H. P message headers into parsed HTTP message headers using the regular expression pattern matching circuit, and providing the parsed HTTP respectively.

"As to claim 20, Ge than teaches a method for processing incoming messages sent by a client develope to a server, comprising:

receiving the income messages using a First-In-First-Out (FIFO) buffer; assembling the incoming messages from a serial to a parallel form using the FIFO buffer (col. 1 mess 39-47, col. 4, lines 11-25); and

concurrent with the a simbling of the incoming messages from a serial

recognizing Hyperte Transfer Protocol (HTTP) message headers embedded in the incoming ressages received by the FIFO buffer using a regular-expression pattern number of thing circuit (col. 1, lines 13-24, col. 1, lines

parsing recognized Hisp message headers into parsed HTTP message headers using the regular-extension pattern matching circuit (col. 1, lines 13-24, col. 1, lines 39-47), and

providing the parsed TP message headers to the server (col. 1, lines

Applicant respectfully disastics because the concurrent, recognizing, parsing, or providing steps are not mentioned the Examiner cited sections of Gehman. Further clarification is requested pursuant to CFR §1.104(c)(2). The relevant section of Gehman is not quoted since the lack of the diffined element will be evident upon inspection as is the

Based on the above, it is resultfully submitted that claim 20 is allowable under 35 U.S.C. 102(e) as not being anticipa by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick 2, supra.

As to claim 21, Applicant respectfully traverses the rejections since the Applicants' claimed combination includes the limitation not disclosed in Gehman of:

"providing a response message to the client device based on a content of the recognized HTTP mention headers."

The Examiner states:

"As to claim 21, Gellin teaches the method as claimed in claim 20 further including:

providing a response sage to the client device based on a content of the recognized HTTP message headers (col. 1, lines 13-24, col. 1, lines 39-47)."

Applicant respectfully disagned because no "recognized HTTP message headers" are mentioned in the Examiner cited scients of Gehman. Further clarification is requested pursuant to 37 CFR §1.104(c)(2). The relevant section of Gehman is not quoted since the lack of the claimed element will be expent upon inspection as is the case for claim 2.

Based on the above, it is resentfully submitted that claim 21 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick ... supra.

As to claim 22, Applicant respectfully traverses the rejections since the Applicants' claimed combination includes the limitation not disclosed in Gehman of:

"the providing the parsed H IP message headers to the server provides the parsed HTTP message headers in a compact form."

The Examiner states:

"As to claim 22, Gettin teaches the method as claimed in claim 20 wherein:

the providing the par HTTP message headers to the server provides the parsed HTTP message headers in a compact form (col. 1, lines 13-24, col. 1, lines 39-47)."

Applicant respectfully disases because no "parsed HTTP message headers" are mentioned in the Examiner cited dions of Gehman. Further clarification is requested pursuant to 37 CFR §1.104(c)(2). The relevant section of Gehman is not quoted since the lack of the claimed element will be sent upon inspection as is the case for claim 2.

Based on the above, it is reconfully submitted that claim 22 is allowable under 35 U.S.C. 102(e) as not being anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick L., supra.

As to claim 23, Applicant rejectfully traverses the rejections since the Applicants' claimed combination includes the lineation not disclosed in Gehman of:

"providing to the server incoming messages that cannot be recognized by the regular-expression partin matching circuit."

The Examiner states:

"As to claim 23, Get in teaches the method as claimed in claim 20 further including:

providing to the serve incoming messages that cannot be recognized by the regular-expression pattern matching circuit (col. 1, lines 13-24, col. 1, lines 39-47)."

Applicant respectfully disages s because no regular-expression pattern matching circuit is mentioned in the Examination cited sections of Gehman. Further clarification is requested pursuant to 37 CFR §1.1 (c)(2). The relevant section of Gehman is not quoted since the lack of the claimed element will be evident upon inspection as is the case for claim 2.

Based on the above, it is resultfully submitted that claim 23 is allowable under 35 U.S.C. 102(e) as not being anticipate by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick 12., supra.

As to claim 24, Applicant respectfully traverses the rejections since the Applicants' claimed combination includes the limitation not disclosed in Gehman of:

"the HTTP message headers that de HTTP cookies."

The Examiner states:

> "As to claim 24, Ge traches the method as claimed in claim 20 wherein:

> the HTTP message there include HTTP cookies (col. 1, lines 13-24)."

Applicant respectfully disagres because no HTTP cookies are mentioned in the Examiner cited sections of Gehman Further clarification is requested pursuant to 37 CFR \$1.104(c)(2). The relevant section Gehman is not quoted since the lack of the claimed element will be evident upon inspection as is the case for claim 2.

Based on the above, it is resultfully submitted that claim 24 is allowable under 35 U.S.C. 102(e) as not being anticipally by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick ..., supra.

As to claim 25, Applicant resectfully traverses the rejections since the Applicants' claimed combination includes the limitation not disclosed in Gehman of:

"receiving the incoming messages using a First-In-First-Out (FIFO) buffer; assembling the incoming messages from a serial to a parallel form using the FIFO buffer:

concurrent with the assemble of the incoming messages from a serial to a parallel

recognizing Hypertex gransfer Protocol (HTTP) message headers embedded in the incoming messages received by the FIFO buffer using a regular-expression page in matching circuit, parsing recognized H is pressage headers into parsed HTTP message headers using the regular expression pattern matching circuit, and providing the parsed and P message headers to the server in a compact form:

providing the parsed TP message headers to the server in a compact form; providing a response message to the client device based on a content of the recognized HTTP message header and providing to the server incoming messages that cannot be recognized by the regular-expression part in matching circuit."

The Examiner states:

"As to claim 25, Ge than a [sic] teaches the method for processing incoming messages sent by a cent device to a server, comprising:

receiving the income messages using a First-In-First-Out (FIFO) buffer (col. 1, lines 39-47, col. 1, lines 11-25);

assembling the income genessages from a serial to a parallel form using the FIFO buffer (col. 1 genessages from a serial to a parallel form (col. 1, lines 39-47, col. 1, lines 39-47),

recognizing Hyperter and the incoming messages from a serial to a parallel form (col. 1, lines 39-47),

recognizing Hyperter and the incoming message headers embedded in the incoming and serial to the incoming message headers embedded in the incoming messages received by the FIFO buffer using a

> regular-expression pattern numbing circuit (col. 1, lines 13-24, col. 1, lines 39-47),

parsing recognized H P message headers into parsed HTTP message headers using the regular-exission pattern matching circuit, and providing the parsed HTTP message headers to the server in a compact form; providing a response message to the click device based on a content of the recognized HTTP message headers (col. nes 13-24, col. 1, lines 39-47); and providing to the server in matching messages that cannot be recognized by the regular-expression page on matching circuit (col. 1, lines 13-24, col. 1, lines 39-47)."

lines 39-47)."

Applicant respectfully disagram because no HTTP message headers are mentioned in the Examiner cited sections of Gehr Further clarification is requested pursuant to 37 CFR §1.104(c)(2). The relevant section Gehman is not quoted since the lack of the claimed element will be evident upon inspecting as is the case for claim 2.

Based on the above, it is restrictly submitted that claim 25 is allowable under 35 U.S.C. 102(e) as not being anticipally by Gehman because of Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick i., supra.

The other references cited by Examiner showing the prior art have been considered th, or suggest, either singularly or in combination, and are not believed to disclose, Applicants' invention as claimed.

Based on the above, it is resultfully submitted that claims 1-25 are allowable under 35 U.S.C. §102(e) as not bei anticipated by Gehman because of Lindemann Maschinenfabrik GmbH v. America Hoist & Derrick Co., supra.

Conclusion

In view of the above, it is suggested that the claims are in condition for allowance and reconsideration of the rejections is pectfully requested. Allowance of claims 1-25 at an early date is solicited.

To the extent necessary, a partion for an extension of time under 37 C.F.R. §1.136 is hereby made. Please charge any strategie in fees due in connection with the filing of this

paper, including any extension of the fees, to Deposit Account No. 08-2025 and please credit any excess fees to such deposit account.

Respectfully submitted,

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